



Science & Technology Facilities Council  
Rutherford Appleton Laboratory

## Microelectronics Support Centre

This is to certify that

**Semih Ramazanoglu**  
*attended the following training course*

## Introduction to Analogue Mixed Signal IC Design

9 - 12 November 2021

*Presented by*

**The Microelectronics Support Centre  
URKI Science and Technology Facilities Council  
Rutherford Appleton Laboratory**

This course provided an overview of design flow theory for analogue-centric mixed signal designs in typical CMOS processes. The main steps for the Big-Analogue mixed signal design flow, including methodology, mixed signal simulation, physical integration of digital blocks, and full chip physical verification were explored through lectures and practical exercises using Cadence tools.

Course Administration



**Microelectronics Support Centre**  
[www.msc.stfc.ac.uk](http://www.msc.stfc.ac.uk)

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